

11-17-00

A

Attorney Docket No. 59182-P005US-10020642Date November 16, 2000

11/16/00
jc66 U.S. PTO

jc690 U.S. PTO
09/714426
11/16/00

From FULBRIGHT & JAWORSKI L.L.P.

2200 Ross Avenue, Suite 2800

Dallas, Texas 75201

214/855-8000

PATENT APPLICATION TRANSMITTAL LETTER"Express Mail" label no. EL315113188USDate of Deposit: November 16, 2000

Box PATENT APPLICATION
Assistant Commissioner
for Patents
Washington, D.C. 20231

I hereby certify that this is being deposited with the United States Postal Service Express "Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231

By: Lisa LynchPrinted/Typed Name: Lisa Lynch

Sir:

Transmitted herewith for filing under 37 C.F.R. 1.53(b) is a(n):

- ☒ Utility
☒ Original patent application,

Inventor(s): **Thomas C. McDermott III, Jim Kleiner**For: **SYSTEM AND METHOD FOR ROUTER MULTICAST CONTROL**

Enclosed are:

1. ☒ 17 pages of written description, claims and abstract.
2. ☒ 2 sheets of drawings.
3. ☒ Combined Declaration and Power of Attorney.
 - (a) ☒ Newly executed (original or copy)
 - (b) ☐ Copy from prior application (37 CFR 1.63(d)) *(for continuation/divisional if Box 5 completed)*

[Note Box 5 below]

4. ☐ Incorporation by Reference *(useable if Box (b) is checked).*

The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied under Box 3(b), is considered as being part of the disclosure of the accompanying application and is hereby incorporated by reference therein.

5. ☐ **If a CONTINUING APPLICATION**, check appropriate box and supply the requisite information:

☐ Continuation ☐ Divisional ☐ Continuation-in-part (CIP) of prior application Serial No:
6. ☒ Assignment Papers (cover sheet and document(s)) of the invention to Chiaro Networks, Ltd.
7. ☒ Applicant claims small entity status. See 37 CFR 1.27.
8. ☐ Information Disclosure Statement and Form PTO-1449. ☐ Copies of IDS Citations.
9. ☐ Preliminary Amendment
10. ☒ Return Receipt Postcard (MPEP 503) *(should be specifically itemized)*

Date November 16, 2000

11. ☐ Certified Copy of Priority Document(s) (if foreign priority is claimed)

12. ☐ Other: _____

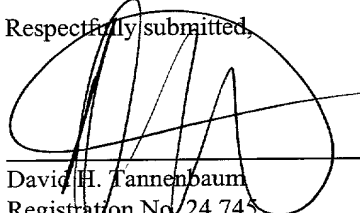
13. **Utility Fee Calculation**

CLAIMS	(1) FOR	(2) NUMBER FILED	(3) NUMBER EXTRA	(4) RATE	(5) CALCULATIONS
	TOTAL CLAIMS (37 C.F.R. § 1.16(c) or (j))	24	- 20 =	4	X \$ 18 = 72.00
	INDEPENDENT CLAIMS (37 C.F.R. § 1.16(b) or (i))	2	- 3 =	0	X \$ 80 =
	MULTIPLE DEPENDENT CLAIMS (if applicable) (37 C.F.R. § 1.16(d))			+	270.00 =
				Basic Fee	710.00
				Total of above Calculations =	782.00
				Reduction by 50% for filing by small entity (Note 37 C.F.R. §§ 1.9, 1.27 & 1.28)	/2
				* Reissue claims in excess of 20 and over original patent ** Reissue independent claims over original patent	
				TOTAL =	\$391.00

Method of Fee Payment

14. ☒ A check in the amount of \$391.00 to cover the filing fee is enclosed.
15. ☒ A check in the amount of \$40.00 to cover the assignment recordal fee is enclosed.
16. ☐ Please charge my Deposit Account No. 06-2380 in the total amount of the filing fee and the assignment recordation fee, if any. **A duplicate of this Transmittal Letter is enclosed, if box checked.**
17. ☒ The Commissioner is hereby authorized to charge any deficiency in the enclosed fees under 37 C.F.R. § 1.16, or to charge any patent application processing fees under 37 C.F.R. § 1.17, or credit any overpayment, to Fulbright & Jaworski L.L.P. Deposit Account No. 06-2380.

Respectfully submitted,


 David H. Tannenbaum
 Registration No. 24,745
 Counsel for Applicant

Date: November 16, 2000

Fulbright & Jaworski L.L.P.
 2200 Ross Avenue, Suite 2800
 Dallas, Texas 75201-2784
 Telephone: 214-855-8333
 Telecopier: 214-855-8200

SYSTEM AND METHOD FOR ROUTER MULTICAST CONTROL

Thomas C. McDermott III
265 Daniel Drive
Plano, Texas 75094
Citizenship: USA

Jim Kleiner
13834 Sprucewood Drive
Dallas, Texas 75240
Citizenship: USA

RELATED APPLICATIONS

This application is related to co-pending and commonly assigned U.S. Application Serial Number [59182-P001US-10020638], entitled "System And Method For IP Router With an Optical Core," to co-pending and commonly assigned U.S. Application Serial Number [59182-P002US-10020639], entitled "System and Method for Router Central Arbitration," to co-pending and commonly assigned U.S. Application Serial Number [59182-P004US-10020641], entitled "System and Method for Router Data Aggregation and Delivery," to co-pending and commonly assigned U.S. Application Serial Number [59182-P006US-10020643], entitled "Timing and Synchronization for an IP Router Using an Optical Switch," to co-pending and commonly assigned U.S. Application Serial Number [59182-P012US-10021641], entitled "Router Network Protection Using Multiple Facility Interfaces," to co-pending and commonly assigned U.S. Application Serial Number [59182-P013US-10021642], entitled "Router Line Card Protection Using One-for-N Redundancy" and to co-pending and commonly assigned U.S. Application Serial Number [59182-P014US-10021643], entitled "Router Switch Fabric Protection Using Forward Error Correction," all filed October 31, 2000, the disclosures of which are incorporated herein by reference.

TECHNICAL FIELD

This application relates to the field of optical communication networks, and particularly to large-scale routers for optical communication networks.

OFFICE OF THE SECRETARY

BACKGROUND

Routers with electronic switch fabrics have the ability to store data packets and then make multiple copies of those packets. In one approach, for example, a packet is copied into an electronic fabric and each output card reads the same copy of that packet. When all of the outputs have read the packet, it can be discarded. Thus the fabric itself is capable of performing a multicast operation, in which one input packet is cloned and copied to multiple outputs.

A router switch fabric that has no inherent storage or replication capability, for example an optical switch fabric, cannot fundamentally multicast packets. There are other approaches that, for example, store a packet on an input line card in electronic form and have the input line card make multiple copies of the packet, sending them to each multicast output as necessary. Although this will function and perform the multicast operation, a drawback is that the packet sits in memory on the input card, and if sufficient numbers of multicast packets arrive, the memory on the input card can be exhausted, or the memory can back up subsequent nonmulticast packets and delay or block them from getting through the switching fabric.

SUMMARY OF THE INVENTION

The present invention is directed to a system and method to transfer a multicast packet out of an input card which may have unicast as well as multicast traffic coming into it, and to remove the multicast packet to a dedicated multicast card. The dedicated multicast card is then responsible for replicating multiple copies of that packet to the various multicast outputs. This approach is applicable to router switching fabrics, for example optical switch fabrics, that have no inherent packet storage or replication capability. A dedicated multicast card is substantially the same kind of card as an input and output line card, but without external facility interface connections.

In an embodiment of the present invention an incoming multicast packet is transferred through the router switching fabric to a dedicated output multicast card substituting for an ordinary output line card, which then converts the packet from an optical packet to an electrical packet and passes it to a dedicated input multicast card substituting for a conventional input line card. There the packet is stored and can be replicated electrically, converted back to an optical packet, and transferred through the router switching fabric to multiple destination output line cards. In a preferred embodiment, the input and output dedicated multicast cards are in reality the same dedicated multicast card.

Transfer of the multicast packet to multiple destinations can occur sequentially over multiple switching cycles or can occur simultaneously. In some embodiments the switching fabric is subdivided into multiple subplanes, such that multiple parallel switching paths exist through the switching fabric. This enables simultaneous parallel multicasting to multiple destinations during a single switching cycle. Some embodiments of the present invention include multiple dedicated multicast cards. This configuration allows rapid expansion of the multicast tree to a large number of destinations.

Various aspects of the invention are described in co-pending and commonly assigned U.S. Application Serial Number [59182-P001US-10020638], entitled "System And Method For IP Router With an Optical Core," co-pending and commonly assigned U.S. Application Serial Number [59182-P002US-10020639], entitled "System and Method for Router Central

Arbitration,” co-pending and commonly assigned U.S. Application Serial Number [59182-P004US-10020641], entitled “System and Method for Router Data Aggregation and Delivery,” co-pending and commonly assigned U.S. Application Serial Number [59182-P006US-10020643], entitled “Timing and Synchronization for an IP Router Using an Optical Switch,” co-pending and commonly assigned U.S. Application Serial Number [59182-P012US-10021641], entitled “Router Network Protection Using Multiple Facility Interfaces,” co-pending and commonly assigned U.S. Application Serial Number [59182-P013US-10021642], entitled “Router Line Card Protection Using One-for-N Redundancy” and co-pending and commonly assigned U.S. Application Serial Number [59182-P014US-10021643], entitled “Router Switch Fabric Protection Using Forward Error Correction,” all filed October 31, 2000, the disclosures of which are incorporated herein by reference.

The foregoing has outlined rather broadly the features and technical advantages of the present invention in order that the detailed description of the invention that follows may be better understood. Additional features and advantages of the invention will be described hereinafter which form the subject of the claims of the invention. It should be appreciated by those skilled in the art that the conception and specific embodiment disclosed may be readily utilized as a basis for modifying or designing other structures for carrying out the same purposes of the present invention. It should also be realized by those skilled in the art that such equivalent constructions do not depart from the spirit and scope of the invention as set forth in the appended claims. The novel features which are believed to be characteristic of the invention, both as to its organization and method of operation, together with further objects and advantages will be better understood from the following description when considered in connection with the accompanying figures. It is to be expressly understood, however, that each of the figures is provided for the purpose of illustration and description only and is not intended as a definition of the limits of the present invention.

5

Fig. 1 is a block diagram illustrating multicast data packet flow through a router system having an optical switching fabric, in accordance with an embodiment of the present invention; and

Fig. 2 is a block diagram illustrating a router embodiment that includes multiple dedicated multicast cards.

DETAILED DESCRIPTION

Fig. 1 is a block diagram illustrating multicast data packet flow through a router system 10 having an optical switching fabric 12, in accordance with an embodiment of the present invention. Optical switching fabric 12 can be partitioned into multiple switching subplanes, for example subplanes 12-1 through 12-3, each having multiple inputs and outputs, which are interconnected dynamically in input/output pair configurations through the optical switch, for example through switching paths 106 and 112-1 through 112-3. The inputs of optical switching fabric 12 are interconnected with multiple input line cards, represented for clarity in Fig. 1 by input line card 14 interconnected through optical link 105 with optical switching fabric 12. Each input line card 14 is in turn interconnected through an electrical link 103 with an input facility interface card 13. Input facility interface card 13 is connected through optical input data path 101 with other network systems, for example peer routers. The outputs of optical switching fabric 12 are interconnected with multiple output line cards, represented for clarity by output line cards 20-1 through 20-N interconnected with optical switching fabric 12 through optical links 113-1 through 113-N. Output line cards 20-1 through 20-N are further interconnected through electrical links 114-1 through 114-N with respective output facility interface cards 21-1 through 21-N, which in turn are connected through respective optical output data paths 115-1 through 115-N with other systems across the network.

In the present embodiment, a dedicated output multicast card 16 is interconnected through an optical link 107 with an output of optical switch fabric 12. Likewise, a dedicated input multicast card 18 is interconnected through an optical link 110-1 with an input of optical switch fabric 12. Dedicated multicast cards 16 and 18 are substantially the same type of cards as respective input line card 14 and output line cards 20-1 through 20-N, but without external facility interface connections. Dedicated multicast cards 16 and 18 are connected together through a data loop 108.

When a packet destined to be multicast (a “multicast packet”) arrives at an input line card 14, the packet header destination is decoded and the multicast packet is accordingly

forwarded through optical links 105 and 107 and through optical switch fabric 12 along switching path 106 to dedicated output multicast card 16. Dedicated output multicast card 16 converts the optical multicast packet to an electrical multicast packet, which is looped back to dedicated input multicast card 18 through data loop 108. Dedicated input multicast card 18 then converts the multicast packet back to an optical multicast packet, which it forwards through optical link 110-1 and along a switching path 112-1 to an output line card 20-1 interconnected with a destination optical switch output through an optical link 113-1. At output line card 20-1 the multicast packet is handled in the same fashion as any other data packet arriving at output line card 20-1.

A system and method for a network router with an optical switching fabric is described in U.S. Application Serial Number [59182-P001US-10020638], cited above. Although for the purpose of clarity corresponding input and output functions are shown on separate circuit cards in separate ½ line shelves, embodiments are described in which corresponding input and output functions are combined on a single circuit card in a single line shelf combining ½ input and output line shelves, thus creating a folded router system configuration. Particularly, input and output packet forwarding line cards can be combined into a single physical circuit card in a single line shelf. In this folded system configuration, dedicated output multicast card 16 and dedicated input multicast card 18 are in fact the same circuit card, and data loop 108 can become simply a conductive trace on that single circuit card.

Once the multicast packet is removed from the original input line card 14, it no longer sits in memory on the input line card, and thus no longer delays or blocks ordinary network traffic. In some embodiments dedicated multicast card 16, 18 makes multiple replicas of a multicast packet and then forwards these replicas through optical switch fabric 12 to multiple switch destinations connected with multiple output line cards 20-1 through 20-N. These replica multicast packets can be forwarded serially during separate switch cycles.

Alternatively, replica multicast packets can be forwarded simultaneously within one switch cycle to multiple destinations, provided that optical switch fabric 12 incorporates m

multiple switching subplanes, for example subplanes 12-1 through 12-3, that support m multiple parallel switching paths, illustrated schematically by switching paths 112-1 through 112-3. In the router system described in U.S. Application Serial Number [59182-P001US-10020638], cited above, for example, a multiple subplane switch fabric architecture supports simultaneous parallel switching paths. This structure allows sending two or more multicast replica packets in parallel simultaneously from one dedicated input multicast card 18 through switch fabric 12 to multiple destination output line cards 20-1 through 20-N along separate paths, for example a path through link 110-1 and then through switching path 112-1 and a separate path through link 110-3 and then through switching path 112-3. If the required multicast output size were six, for example, one multicast packet could be sent from input line card 14 to a dedicated multicast card 16, 18, which could replicate the packet and transmit up to three multicast replicas in parallel to three destination output line cards, for example output line cards 20-1 through 20-3, during one switch cycle, and then during a subsequent switch cycle transmit another three multicast replicas in parallel to three additional destination output line cards, for example output line cards 20-4 through 20-6. This multicast operation requires three switch cycles, namely one cycle to send the original multicast packet from the input line card to the dedicated multicast card, and two additional cycles to transmit multicast replicas to the six destinations at three replicas per cycle. Because the first cycle is pipelined, the actual delay is two cycles.

The number of times t that a multicast packet is sent through the switch fabric is given by an expression

$$t = 1 + \text{CEILING}\left(\frac{N}{m}\right)$$

one time to go from the input line card to the dedicated multicast card and then

$\text{CEILING}\left(\frac{N}{m}\right)$ times, where N is the number of destination line cards, m is the

number of simultaneous parallel switching paths, and the *CEILING* function rounds up a value to the next larger integer.

Fig. 2 is a block diagram illustrating a router embodiment 50 that includes M multiple dedicated multicast cards 16-1 through 16- M and 18-1 through 18- M . Switch fabric 12 is partitioned into m multiple subplanes, for example subplanes 12-1 through 12-3, thus supporting m multiple simultaneous switching paths, for example switching paths 106-1 through 106- m , 112-1 through 112- m , and 116-1 through 116- m . In a folded router geometry, multicast cards 16-1 and 18-1, and connecting data loop 108-1 occupy a single circuit card. Similarly multicast cards 16- M and 18- M and connecting data loop 108- M occupy a single circuit card. In the example of Fig. 2, a multicast packet is sent by input line card 14 simultaneously through parallel switching paths 106-1 through 106- m across switch fabric 12 to M dedicated multicast cards 16-1, 18-1 through 16- M , 18- M , where the packet is replicated at each multicast card and then sent in m parallel replica packets from each dedicated multicast card 16-1, 18-1 through 16- M , 18- M through simultaneous switching paths 112-1 through 112- m and 116-1 through 116- m respectively across switch fabric 12 to N destination line cards 20-1 through 20- N . This multiple replication provides an N -to-one multicast tree expansion. If two dedicated multicast cards 16- M , 18- M can each send three replica packets, then three replicas multiplied by two multicast cards would provide six-to-one multicast tree expansion. This procedure improves the fan out and reduces the time delay, since more data is multicast within a single switch cycle.

In the more general case with N multiple output destinations, M multiple dedicated multicast cards, and m simultaneous parallel switching paths, the number of times t that a multicast packet goes through the switch fabric is given by an expression

$$t = \text{CEILING}\left(\frac{M}{m}\right) + \text{CEILING}\left(\frac{N}{mM}\right),$$

where variables and functions are as defined above. If for example $M = 2$, $m = 3$, and $N = 100$, then $t = 18$ switch cycles, whereas if $M = 5$, $m = 3$, and $N = 100$, then $t = 9$ switch cycles, which is only half of the previously calculated delay. The m parallel switching paths limited by the subplane structure of the switch fabric refer only to those paths that originate

from a common input, for example either a common input line card or a common dedicated multicast input card. The M different multiple dedicated multicast cards can be increased independent of this limitation. The M different multiple dedicated multicast cards can then each originate a set of m simultaneous parallel switching paths through the switch fabric.

5 Since adding a dedicated multicast card is a lower cost option than adding a switch subplane, multiple dedicated multicast cards provide an advantageous approach to increasing the multicast tree expansion with minimum switch cycle delay.

This means that for example in Fig. 2, one input line card 14 could send a multicast packet simultaneously to M different dedicated multicast cards, which then could perform multiple replication. Thus, the number of multicast cards times the number of available simultaneous parallel switching paths expands the multicast tree for a single switch cycle. This affords the flexibility to install as many multicast cards as is necessary to support the required multicast tree expansion, with an option to install fewer if desired.

Although the present invention and its advantages have been described in detail, it should be understood that various changes, substitutions and alterations can be made herein without departing from the spirit and scope of the invention as defined by the appended claims. Moreover, the scope of the present application is not intended to be limited to the particular embodiments of the process, machine, manufacture, composition of matter, means, methods and steps described in the specification. As one of ordinary skill in the art will
20 readily appreciate from the disclosure of the present invention, processes, machines, manufacture, compositions of matter, means, methods, or steps, presently existing or later to be developed that perform substantially the same function or achieve substantially the same result as the corresponding embodiments described herein may be utilized according to the present invention. Accordingly, the appended claims are intended to include within their
25 scope such processes, machines, manufacture, compositions of matter, means, methods, or steps.

WHAT IS CLAIMED IS:

1. A network router system comprising:

an optical switching fabric having multiple inputs and multiple outputs connected through multiple switching paths;

multiple input line cards interconnected with said multiple inputs of said switching fabric;

multiple output line cards interconnected with said multiple outputs of said switching fabric;

a plurality of facility interface cards disposed such that each of said multiple input and output cards is interconnected with a facility interface card;

a dedicated multicast output card interconnected substitutionally for said output line card with at least one said switch fabric output; and

a dedicated multicast input card interconnected substitutionally for said input line card with at least one said switch fabric input, such that said dedicated multicast input card is connected with said dedicated multicast output card through a data path and such that said dedicated multicast input and output cards have no facility interface connection.

2. The router system of claim 1 wherein said dedicated multicast input card, said dedicated multicast output card, and said data path are combined into a single dedicated multicast card.

3. The router system of claim 1 wherein said optical switching fabric is partitioned into a plurality of working subplanes.

4. The router system of claim 1 further comprising a plurality of said dedicated multicast input cards and a plurality of said dedicated multicast output cards, such that each said multicast input card is connected with one said dedicated multicast output card.

5. The router system of claim 1 wherein said optical switching fabric contains multiple inputs and multiple outputs connected through multiple parallel switching paths.

009182-P005US-10020642

6. A method of multicasting a data packet from a single input to multiple outputs of a router system having an optical switching fabric, comprising:

transferring the data packet to be multicast (multicast packet) from a router input to a dedicated multicast output card;

5 transferring said multicast packet from said dedicated multicast output card to a dedicated multicast input card;

storing said multicast packet on said dedicated multicast input card; and

transferring said stored multicast packet from said dedicated multicast input card to said multiple outputs of said router system.

7. The method of claim 6 wherein said dedicated multicast output card and said dedicated multicast input card are the same dedicated multicast card.

8. The method of claim 6 wherein said multicast packet is transferred to said dedicated multicast output card through said optical switching fabric.

9. The method of claim 6 wherein said multicast packet is converted from an optical packet to an electrical packet at said dedicated multicast output card.

10. The method of claim 6 wherein said multicast packet is replicated to produce a replica packet.

11. The method of claim 10 wherein said replica packet is produced at a location selected from the group consisting of said dedicated multicast output card and said dedicated multicast input card.

12. The method of claim 6 wherein said multicast packet is converted from an electrical packet to an optical packet at said dedicated multicast input card.

13. The method of claim 6 wherein said multicast packet is transferred from said dedicated multicast input card to said multiple outputs through said optical switching fabric.

14. The method of claim 6 wherein said multicast packet is transferred to said multiple outputs serially over a period of multiple switching cycles of said optical switching fabric.

15. The method of claim 10 wherein said optical switching fabric contains multiple inputs and multiple outputs connected through multiple parallel switching paths.

16. The method of claim 15 wherein multiple replica packets are transferred in parallel to said multiple outputs during a single cycle of said optical switching fabric.

17. The method of claim 16 wherein said multicast packet is sent through said switching fabric $1 + \text{CEILING}\left(\frac{N}{m}\right)$ times wherein N is the quantity of said multiple outputs, wherein m is the quantity of said multiple parallel switching paths, and wherein said *CEILING* function rounds the value of a variable up to the next higher integer.

18. The method of claim 8 wherein said multicast packet is transferred substantially simultaneously to a plurality of said dedicated multicast output cards through said optical switching fabric.

19. The method of claim 18 wherein said multicast packet is transferred from a plurality of said dedicated multicast input cards to said multiple outputs through said optical switching fabric.

20. The method of claim 19 wherein said multicast packet is transferred to said multiple outputs serially over a period of multiple switching cycles of said optical switching fabric.

21. The method of claim 19 wherein said optical switching fabric contains multiple inputs and multiple outputs connected through multiple parallel switching paths.

22. The method of claim 21 wherein said multicast packet is replicated to produce a replica packet.

23. The method of claim 22 wherein multiple replica packets are transferred substantially simultaneously in parallel from a plurality of said dedicated multicast input cards to said multiple outputs during a single switching cycle of said optical switching fabric.

24. The method of claim 23 wherein said multicast packet is sent through said switching fabric $CEILING(\frac{M}{m}) + CEILING(\frac{N}{mM})$ times, wherein N is the quantity of said multiple outputs, wherein M is the quantity of said plurality of dedicated multicast cards, wherein m is the quantity of said multiple parallel switching paths, and wherein said *CEILING* function rounds the value of a variable up to the next higher integer.

SYSTEM AND METHOD FOR ROUTER MULTICAST CONTROL**ABSTRACT OF THE DISCLOSURE**

A multicast packet is transferred through a switching fabric from an input line card to a dedicated multicast card, which is substantially the same as an input and output line card, but without external facility interfaces. A dedicated output multicast card converts the multicast packet from an optical to an electrical packet and passes it to a dedicated input multicast card, where the packet is replicated electrically, converted back to an optical packet, and then transferred through the switching fabric to multiple destinations. In some embodiments, input and output dedicated multicast cards are actually a single card. Transfer of the multicast packet to multiple destinations occurs sequentially or simultaneously during a single switching cycle, if multiple parallel switching paths exist through the switching fabric. Some embodiments include multiple dedicated multicast cards, allowing rapid simultaneous expansion of the multicast tree.

11/13/00



OFFICE ACTION

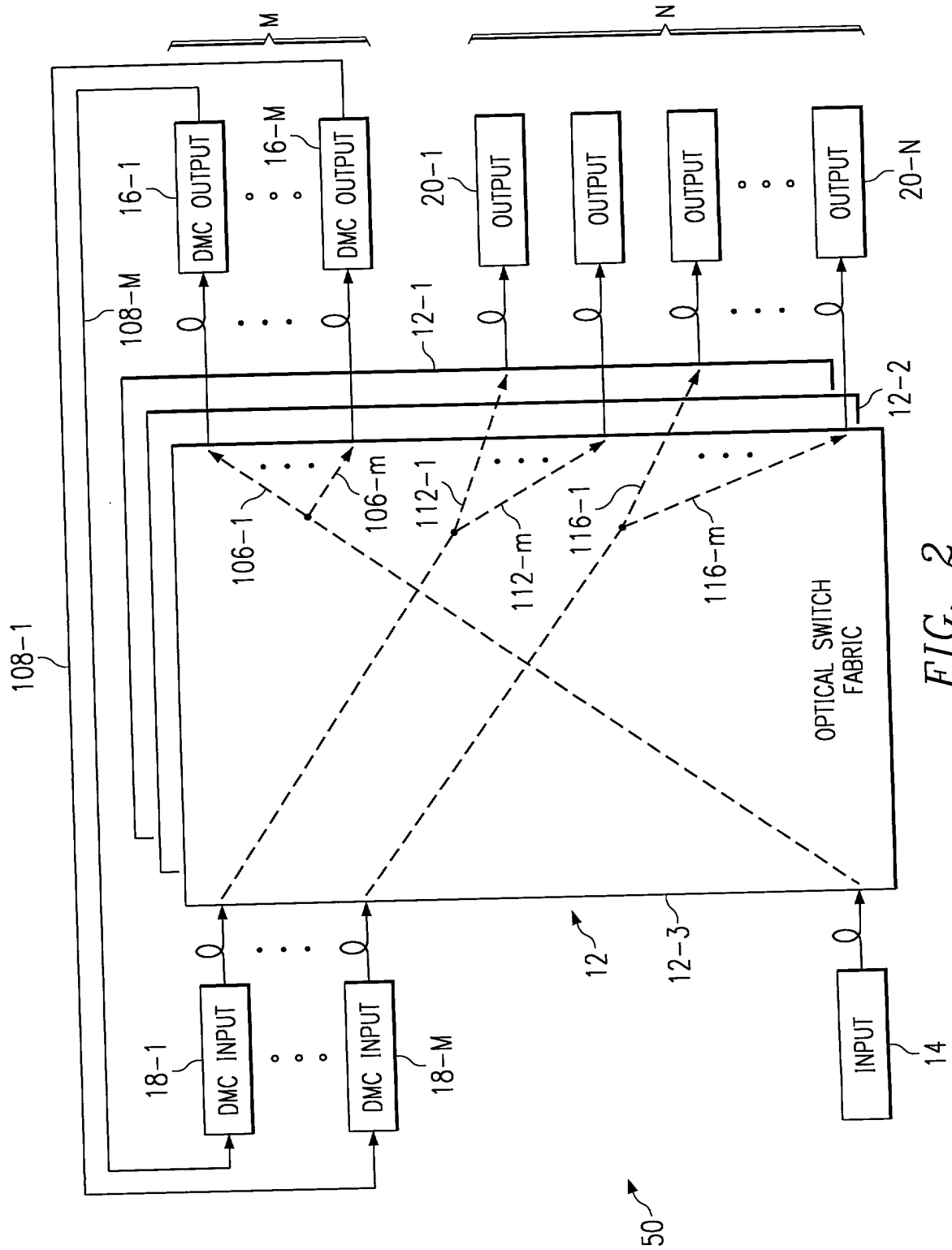


FIG. 2

COMBINED DECLARATION AND POWER OF ATTORNEY

(ORIGINAL, DESIGN, NATIONAL STAGE OF PCT, SUPPLEMENTAL, DIVISIONAL,
CONTINUATION, OR C-I-P)

As a below named inventor, I hereby declare that:

TYPE OF DECLARATION

This declaration is of the following type:

- ☒ original.
- ☐ design.
- ☐ supplemental.
- ☐ national stage of PCT.
- ☐ divisional.
- ☐ continuation.
- ☐ continuation-in-part (C-I-P).

INVENTORSHIP IDENTIFICATION

My residence, post office address and citizenship are as stated below, next to my name. I believe that I am the original, first and sole inventor (*if only one name is listed below*) or an original, first and joint inventor (*if plural names are listed below*) of the subject matter that is claimed, and for which a patent is sought on the invention entitled:

TITLE OF INVENTION

SYSTEM AND METHOD FOR ROUTER MULTICAST CONTROL

SPECIFICATION IDENTIFICATION

The specification of which:

- (a) ☒ is attached hereto.
- (b) ☐ was filed on _____, as ☐ Serial No. 0 / _____ or
☐ _____ and was amended on _____ (*if applicable*).
- (c) ☐ was described and claimed in PCT International Application No. _____ filed on _____ and as amended under PCT Article 19 on _____ (*if any*).

SUPPLEMENTAL DECLARATION (37 CFR 1.67(b))

- ☐ I hereby declare that the subject matter of the
- ☐ attached amendment
- ☐ amendment filed on _____.

was part of my/our invention and was invented before the filing date of the original application, above identified, for such invention.

ACKNOWLEDGMENT OF REVIEW OF PAPERS AND DUTY OF CANDOR

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information, which is material to patentability as defined in 37, Code of Federal Regulations, § 1.56,

- ☐ in compliance with this duty, there is attached an information disclosure statement, in accordance with 37 CFR 1.98.

PRIORITY CLAIM (35 U.S.C. § 119(a)-(d))

I hereby claim foreign priority benefits under Title 35, United States Code, § 119(a)-(d) of any foreign application(s) for patent or inventor's certificate or of any PCT international application(s) designating at least one country other than the United States of America listed below and have also identified below any foreign application(s) for patent or inventor's certificate or any PCT international application(s) designating at least one country other than the United States of America filed by me on the same subject matter having a filing date before that of the application(s) of which priority is claimed.

- (d) ☒ no such applications have been filed.
- (e) ☐ such applications have been filed as follows.

**PRIOR FOREIGN/PCT APPLICATION(S) FILED WITHIN 12 MONTHS
(6 MONTHS FOR DESIGN) PRIOR TO THIS APPLICATION
AND ANY PRIORITY CLAIMS UNDER 35 U.S.C. § 119(a)-(d)**

COUNTRY (OR INDICATE IF PCT)	APPLICATION NUMBER	DATE OF FILING DAY, MONTH, YEAR	PRIORITY CLAIMED UNDER 35 USC 119	
			[] Yes	[] No
			[] Yes	[] No
			[] Yes	[] No

CLAIM FOR BENEFIT OF PRIOR U.S. PROVISIONAL APPLICATION(S)
(35 U.S.C. § 119(e))

I hereby claim the benefit under Title 35, United States Code, § 119(e) of any United States provisional application(s) listed below:

PROVISIONAL APPLICATION NUMBER	FILING DATE
____/____/____	____/____/____
____/____/____	____/____/____
____/____/____	____/____/____

CLAIM FOR BENEFIT OF EARLIER U.S./PCT APPLICATION(S)
UNDER 35 U.S.C. § 120

☐ I hereby claim the benefit under Title 35, United States Code § 120 of any United States application(s) or § 365(b) of any PCT international application designating the United States of America, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior U.S. or PCT international application in the manner provided by the first paragraph of Title 35, U.S.C. § 112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations § 1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application.

APPLICATION SERIAL	FILING DATE	STATUS

ALL FOREIGN APPLICATION(S), IF ANY, FILED MORE THAN 12 MONTHS
(6 MONTHS FOR DESIGN) PRIOR TO THIS U.S. APPLICATION

POWER OF ATTORNEY

I hereby appoint the following practitioner(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith:

David H. Tannenbaum, Reg. No. 24,745;
Michael A. Papalas, Reg. No. 40,381;
R. Ross Viguet, Reg. No. 42,203;
Michael J. Fogarty, III, Reg. No. 42,541;
Jody Bishop, Reg. No. 44,034;
Thomas J. Meaney, Reg. No. 41,990;
Matthew Jones, Reg. No. 44,810 and
William B. Tiffany, Reg. No. 41,347.

SEND CORRESPONDENCE TO

DIRECT TELEPHONE CALLS TO:

David H. Tannenbaum
FULBRIGHT & JAWORSKI L.L.P.
2200 Ross Avenue, Suite 2800
Dallas, Texas 75201.

David H. Tannenbaum
(214) 855-8333

DECLARATION

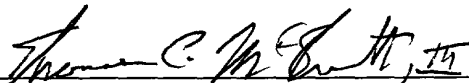
I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

SIGNATURE(S)

NOTE: Carefully indicate the family (or last) name, as it should appear on the filing receipt and all other document.

Full name of sole or first inventor: Thomas C. McDermott III

Inventor's signature :



Country of Citizenship: U.S.A.

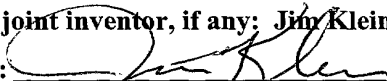
Date: 15-NOV-2000

Residence: 265 Daniel Drive, Plano, Texas 75094

Post Office Address: 265 Daniel Drive, Plano, Texas 75094

Full name of second joint inventor, if any: Jim Kleiner

Inventor's signature:



Country of Citizenship: U.S.A.

Date: 11.15.00

Residence: 13834 Sprucewood Drive, Dallas, Texas 75240

Post Office Address: 13834 Sprucewood Drive, Dallas, Texas 75240